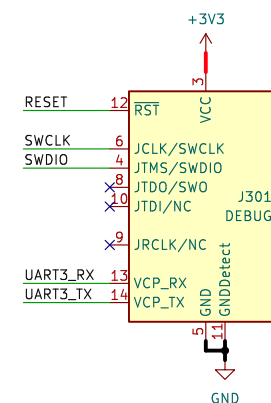
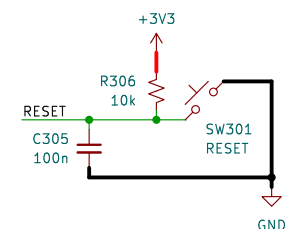
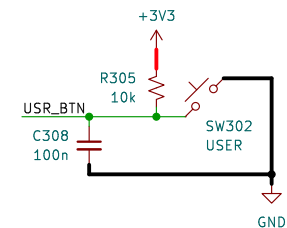
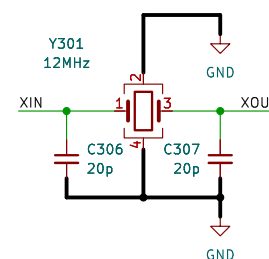
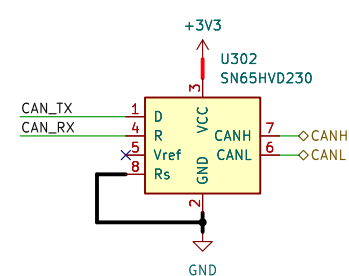
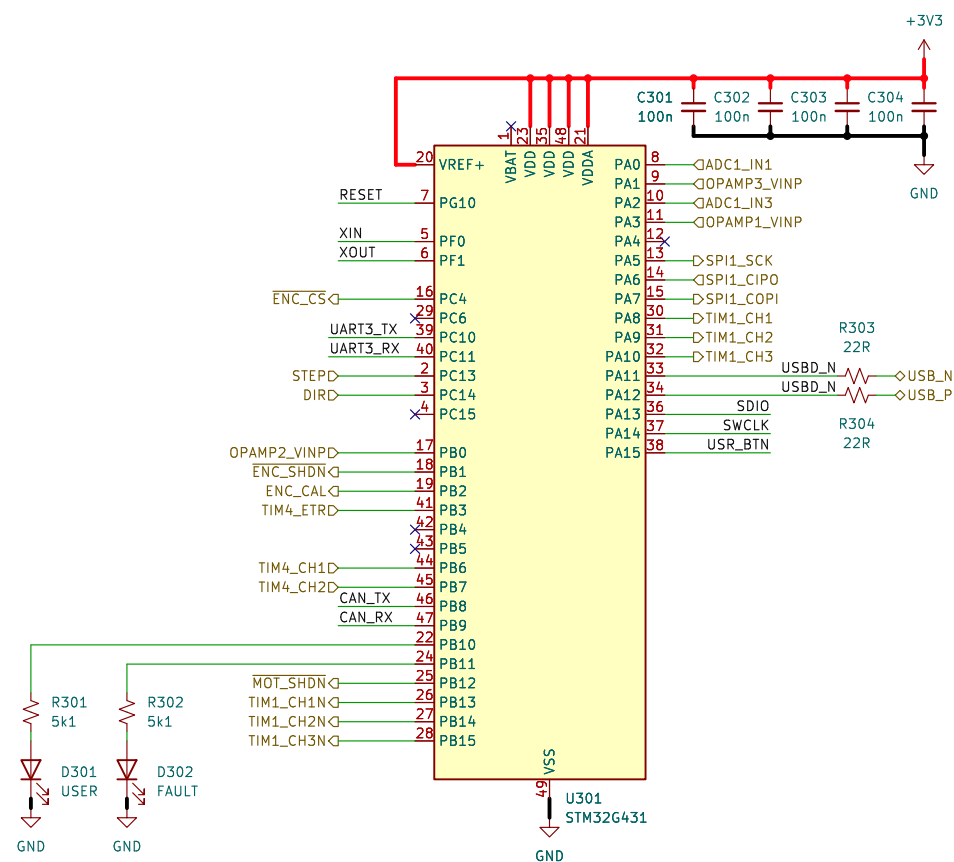


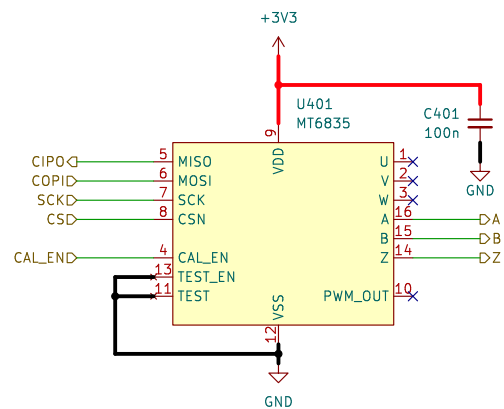
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Rev: 0.1
Id: 3/8



matei repair lab

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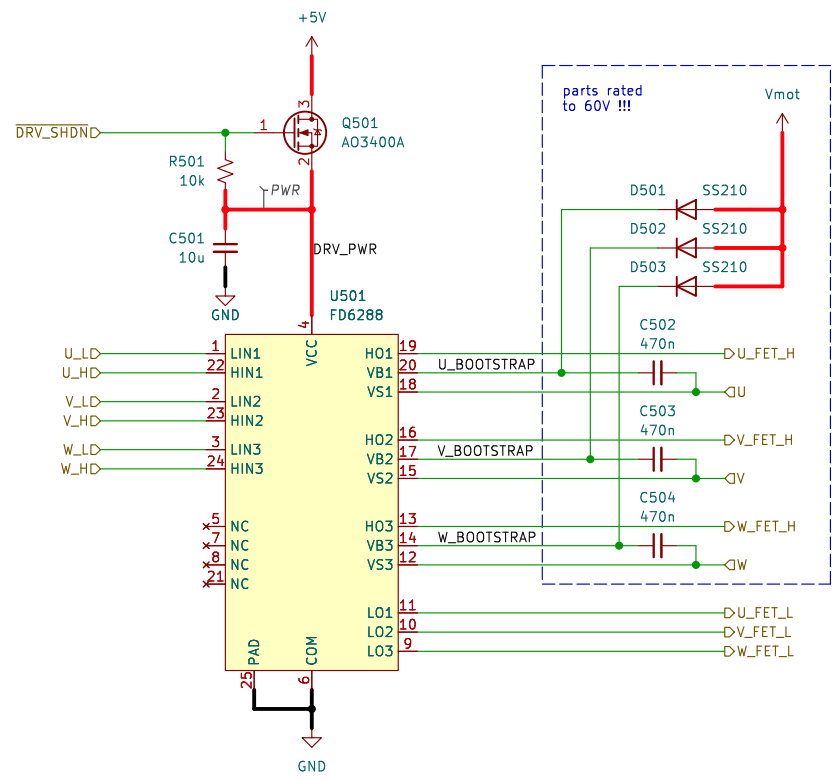
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Size: A4 Date: 2023-10-11

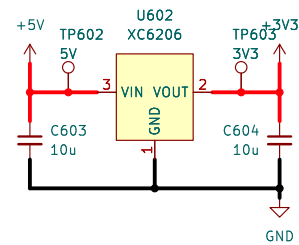
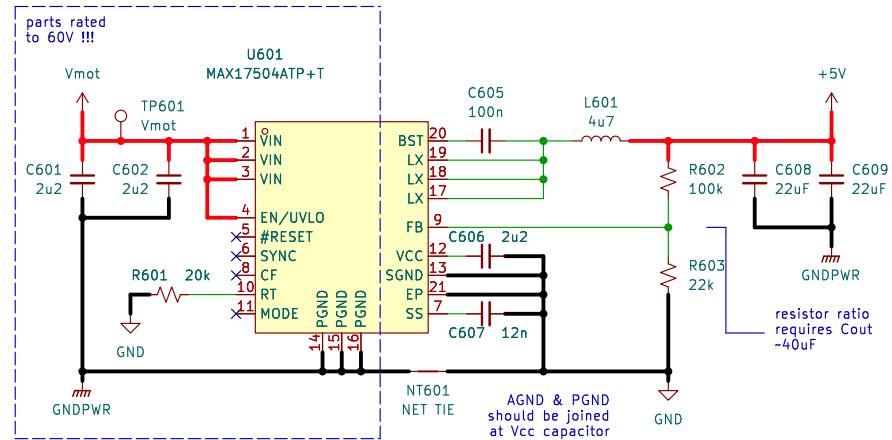
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Rev: 0.1

Id: 4/8



matei repair lab		
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Size: A4	Date: 2023-10-11	Rev: 0.1
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Title: POWER SUPPLY & FILTERING

Size: A4

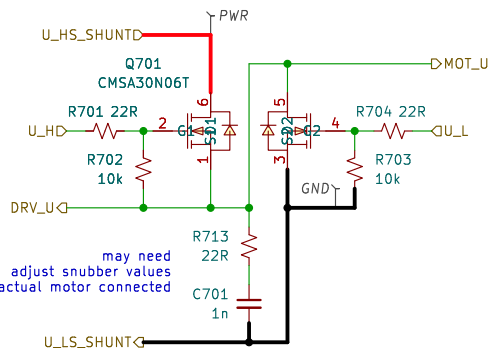
Date: 2023-10-11

Rev: 0.1

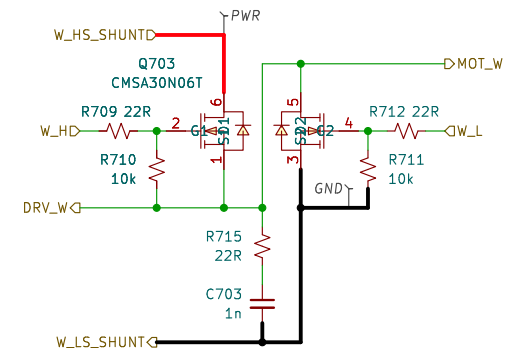
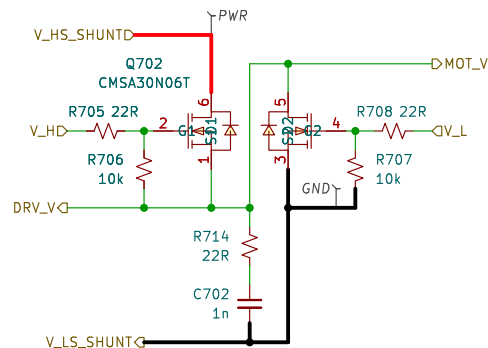
KiCad E.D.A. kicad 7.0.8

Id: 6/8

parts rated
to 60V !!!



may need
adjust snubber values
with actual motor connected



Seven steps to calculate R-C Snubber (with Example)		
Steps	Description	Example
Step 1	Measure the oscillation frequency (f_0) of the V_{DS} ringing with no RC snubber. See Figure 3.	$f_0 = \frac{1}{82ns} = 12.2 MHz$
Step 2	Add a capacitor (C_1) in parallel with the rectifier or FET and measure the shifted oscillation frequency (f_1). Select a C_1 value that is several times larger than the rectifier's stated typical capacitance at full-reverse voltage in the datasheet. In this example, the rectifier's capacitance is 22pF, so I chose a 100pF value for C_1 . A frequency shift of at least 50% is reasonable. See Figure 4.	$C_1 = 100pF$ $f_1 = \frac{1}{90ns} = 11.1 MHz$
Step 3	Calculate the frequency shift ratio: $m = \frac{f_0}{f_1}$	$m = \frac{12.2 MHz}{11.1 MHz} = 1.1$
Step 4	Calculate the circuit's parasitic capacitance: $C_0 = \frac{C_1}{(m^2 - 1)}$	$C_0 = \frac{100pF}{(1.1^2 - 1)} = 0.48nF$
Step 5	Calculate the circuit's parasitic inductance: $L = \frac{(m^2 - 1)}{(2\pi f_0)^2 C_1}$	$L = \frac{(1.1^2 - 1)}{(2\pi \cdot 12.2 MHz)^2 (100pF)} = 0.36\mu H$
Step 6	Calculate the starting snubber capacitor value: $C_{snub} = 3C_0$	$C_{snub} = 3(0.48nF) = 1440pF$
Step 7	Calculate the starter snubber resistor value: $R_{snub} = \sqrt{\frac{L}{C_0}}$	$R_{snub} = \sqrt{\frac{0.36\mu H}{0.48nF}} = 27\Omega$

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Sheet: /fets/
File: fets.kicad_sch

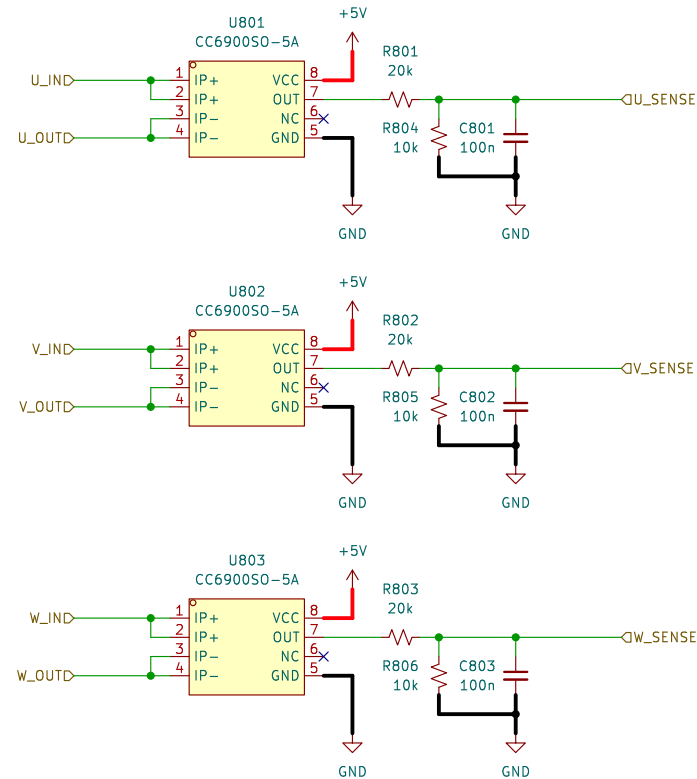
Title: POWER STAGE

Size: A4 Date: 2023-10-11

KiCad E.D.A. kicad 7.0.8

Rev: 0.1

Id: 7/8



5A model
410mV/A sensitivity
expected current $\pm 1A$ (2A peak)
820mV range \rightarrow 3.3V
need gain of ~ 4 at G431 OP-AMP input
for full resolution

due to "hybrid" phase connection, it may be possible
to have 8x gain on the independent phases, and
just this 4x gain on the center tap.